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Oguz Ergin, Deniz Balkan, Kanad Ghose, Dmitry Ponomarev
December 2004 **Proceedings of the 37th annual IEEE/ACM International Symposium on Microarchitecture MICRO 37**
Publisher: IEEE Computer Society
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> A large percentage of computed results have fewer significant bits compared to the full width of a register. We exploit this fact to pack multiple results into a single physical register to reduce the pressure on the register file in a superscalar processor. Two schemes for dynamically packing multiple "narrow-width" results into partitions within a single register are evaluated. The first scheme is conservative and allocates a full-width register for a computed result. If the computed result tu ...

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J. Dido, N. Geraudie, L. Loiseau, O. Payeur, Y. Savaria, D. Poirier
February 2002 **Proceedings of the 2002 ACM/SIGDA tenth international symposium on Field-programmable gate arrays**
Publisher: ACM Press
Full text available: 📄 pdf(207.90 KB)   Additional Information: full citation, abstract, references, citings

> Video signal processing requires complex algorithms performing many basic operations on a video stream. To perform these calculations in real-time in a FPGA, we must use innovative structures to meet speed requirements while managing complexity. As part of a project aiming at the development of a video noise reducer, we developed an optimized processing stream that required some floating-point calculations. This paper presents the rationale for developing a floating-point unit, justifies the dat ...
>
> **Keywords:** FPGA, data-path optimization, floating-point, floating-point/fixed-point conversion, hardware division, hyardware optimization, video-processing

183 Efficient algorithms for processing XPath queries   ⬛
Georg Gottlob, Christoph Koch, Reinhard Pichler
June 2005 **ACM Transactions on Database Systems (TODS)**, Volume 30 Issue 2
Publisher: ACM Press
Full text available: 📄 pdf(721.61 KB)   Additional Information: full citation, abstract, references, index terms

> Our experimental analysis of several popular XPath processors reveals a striking fact: